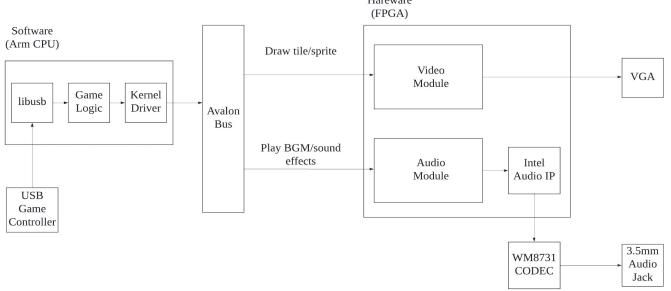
Bubble Bobble

By Hongzheng Zhu, Qingyuan Liu, Ke Liu, Lance Chou

Overall structure:

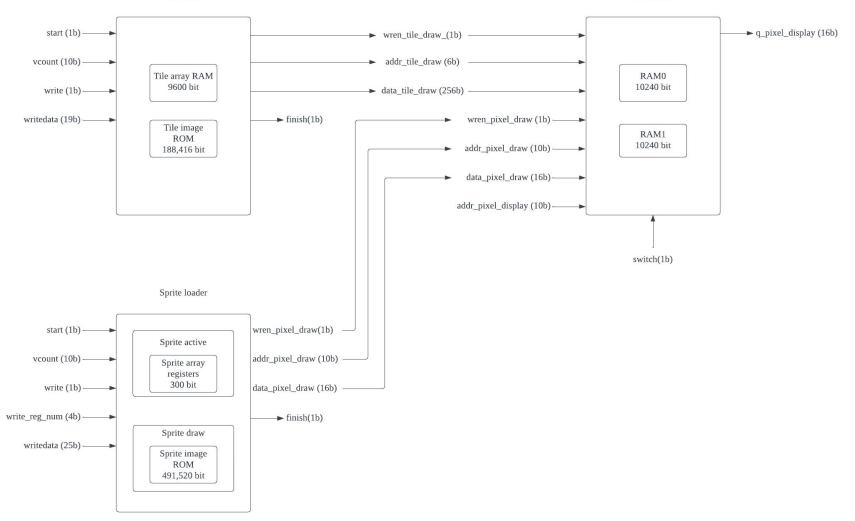


Hareware

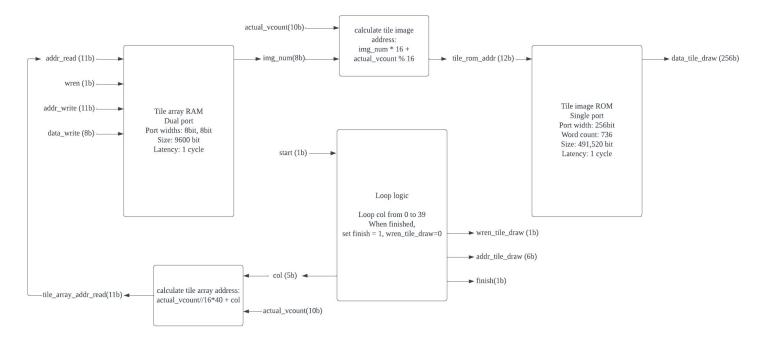
VGA control: vga top module

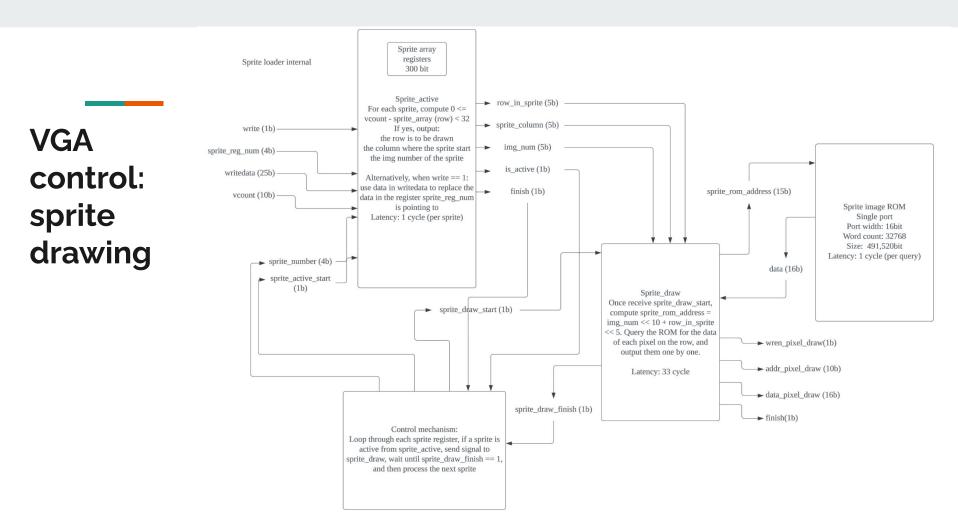
Tile loader

Linebuffer



VGA control: tile drawing





VGA HW/SW interface

// def of argument for tiles

typedef struct {

unsigned char r;

unsigned char c;

unsigned char n;

}vga_top_arg_t;

r 5bit c 6bit n 8bit

 $\ensuremath{\textit{//}}\xspace$ def of argument for sprites

typedef struct {

unsigned char active;

unsigned short r;

unsigned short c;

unsigned char n;

unsigned short register_n;

}vga_top_arg_s;

active 1bit	r 9bit	c 10bit	n 5bit
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Total 25 bit

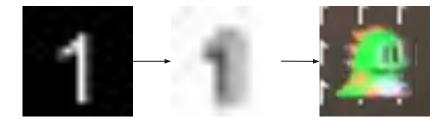
Total 19 bit

Transparent

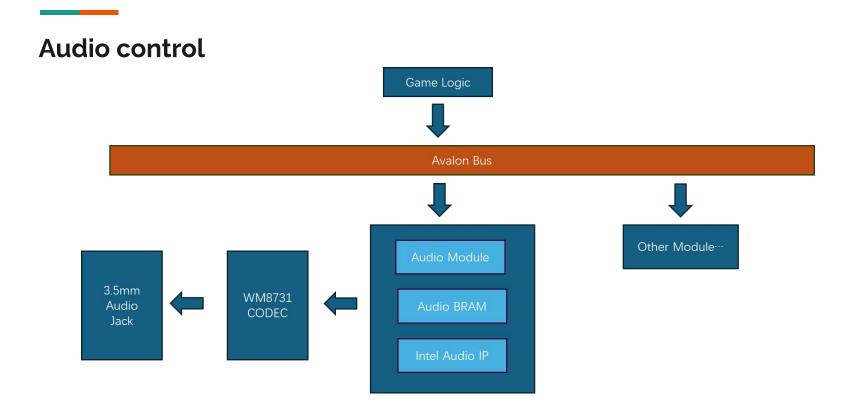


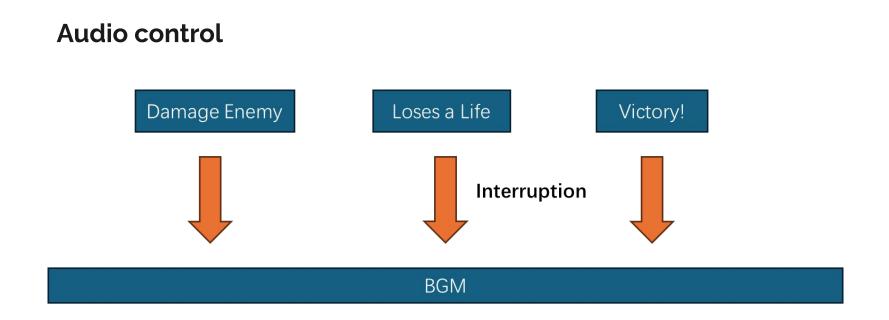






)021 : 100010001000100010001000100010001000	
)022 : 1000100010001000100010001000100010001	
)023:10001000100010001000100010001000100010	
)024 : 100010001000100010001000EC936FED48010001000100010001000100011000	ς.
)025 : 100010001000100010001000EC93EFFF27EC6C81000100010001000100011000;	;
)026 : 100010001000100010001000EC93EFFF6EC9CB7F25A400010001000100011000	I;
)027 : 100010001000100010001000EC93EFFF0001C813012400010001000100011000;	;
)028 : 100010001000100010001000EC93EFFF00010001000100010001000100011000;	
)029 : 100010001000100010001000EC93EFFF0001000100010001000100010001000;	
02A : 10001000100010001000EC93EFFF000100010001000100010001000100010001	;
)02B : 100010001000100010001000EC93EFFF0001000100010001000100010001000;	
)02C : 100010001000100010001000EC93EFFF0001000100010001000100010001000;	;
002D : 100010001000100010001000EC93EFFF000100010001000100010001000100010001	
)02E : 1000100010001000100010001000100010001	
)02F : 1000100010001000100010001000100010001	
)030 : 100010001000100010001000100010001000	
)031:100010001000100010001000100010001000	
)032:10001000100010001000100010001000100010	
)033 : 1000100010001000100010001000100010001	





Audio HW/SW interface

typedef struct {

unsigned char play;

}fpga_audio_arg_t;

#define FPGA_AUDIO_BGM_STARTSTOP_IOW(FPGA_AUDIO_MAGIC, 1, fpga_audio_arg_t*)

#define FPGA_AUDIO_SET_AUDIO_ADDR _IOW(FPGA_AUDIO_MAGIC, 2, fpga_audio_arg_t*)

Controller

The controller communicates with a 8 bytes protocol via USB, mapped as below

Constant	Constant	Constant	Left/right arrow	Up/down arrow	X/Y/A/B	Rib/Select/Start	Constant
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These keys are mapped to specific interactions in the game:

Left arrow: move left

Right arrow: move right

A: shoot bubble

B: jump

struct controller_output_packet {
 short updown;
 short leftright;
 uint8_t select;
 uint8_t start;
 uint8_t left_rib;
 uint8_t right_rib;
 uint8_t x;
 uint8_t y;
 uint8_t a;
 uint8_t b;
}



Game logic

8 levels with different maps.

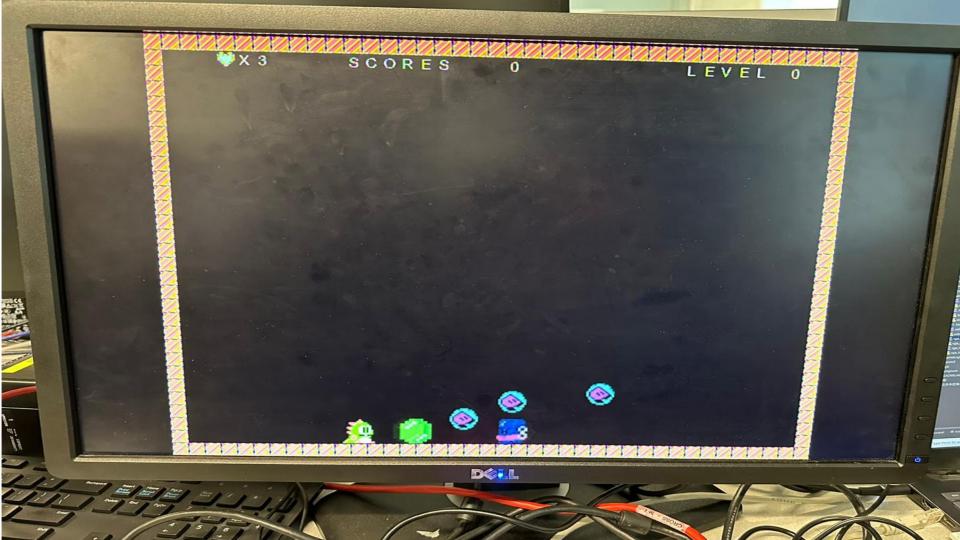
Enemy generation and movement.

Attack.

Collision detection : Wall, floor, bubbles, enemy, character, reward.

Requirements to move to the next level.

Winning and defeat condition.



Demonstration